

LLM Sem.-2 Examination**411 E - IPR****Ind Design, Semiconductor Lay Out Design****Time : 2-30 Hours]****May-2025****[Max. Marks : 100****Answer all the questions:**

Q1. "Definition of Design under the Design Act, 2000 is most exhaustive" Critically discuss the statement. 25

OR

Q1 (a) Write a note on: Industrial and International Exhibition. 13

(b) Write a note on: Solely judged by eye of the customer. 12

Q2. Discuss infringement of design and its remedy under the Design Act 2000. 25

OR

Q2. (a) Briefly discuss the International Law regulating 'Design'. 13

(b) Discuss precisely the subject-matter of protection of Design. 12

Q3. Critically discuss the salient features of the semiconductor Integrated Layout Design Act 2000. 25

OR

Q3. Critically discuss the IPR aspect of semi-conductor Integrated ~~Circuit~~ Layout Design and its need for protection. 25

Circuit

Q4. Write Short Note (Any two) 25

- 1) Washington Treaty
- 2) Assignment and Transmission of Layout Design
- 3) Infringement of Layout Design
- 4) TRIPS Agreement and Integrated Circuit Layout Design