0706E435

M.Sc Sem.-2 Examination

P - 407

Time: 2-00 Hours]

Electronics
June 2022

[Max. Marks: 50

Instruction		
1. At 2. Qı	tempt any THREE questions out of first EIGHT questions in SECTION-I. lestion NINE in SECTION – II is compulsory.	
•		
Q-1 (A	SECTION-I Explain structure and properties of E- plane tee.	
В	properties of L- plane tee.	7
	plane tee.	7
Q-2 (A	Write a short note on magic tee.	
(B	What is directional coupler? What are the different types of directional	7
	couplers: Draw their structures.	7
	Explain working of a directional coupler.	
Q-3 (A)	With neat diagram describe construction and working of reflex klystron.	_
(B)	Obtain equation for bunching parameter of reflex klystron.	7 7
Q-4 (A)	With neat diagram describe construction and working of two cavity klystron.	
(B)	S Working the training the training of the training	7
O E (A)		7
Q-5 (A)	remains phonomenon in ucian will necessary from a	7
	Explain capacitance and charging time in frequency limitations of transistors.	7
Q-6 (A)	in the production of the uctain.	77
(B)	Discuss Kirk effect and Webster effect in detail.	7 7
Q-7 (A)	In case of Fabrication of p-n junction, explain the thermal oxidation in detail.	
(B)	State types of metal-semiconductor Junctions and explain the Schottky barrier	7 7
	in detail.	,
Q-8 (A)	Explain the ohmic and rectifying contacts in detail.	_
(B)	Explain MOSFET structure in detail.	7
		,
Q-9	Select correct answer from the given options (Each question is of ONE mark)	
		8
	is a microwave passive device.	
	(a) Reflex klystron(b) Directional coupler	
	(c) Travelling Wave Tube	
	(d) Gunn diode	
2	In H-plane tee, the side arm is	
<i>22</i>	(a) multiplier arm	
	(b) magic arm	
	(c) subtractor arm	
	(d) adder arm	

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3	Apple gate diagram is a (a) Density against time plot. (b) Distance against time plot (c) Voltage against time plot (d) Velocity against time plot
4	Mode curves of a reflex klystron are (a) the electronic admittance spiral curves (b) the distance against time plots (c) the power output and frequency characteristics plots (d) the radiation pattern plots
5	In case of diffusion, if dopant atoms are supplied continuously, such that the concentration at the surface is maintained at constant value, the distribution follows what is called (a) N-channel error function (b) complementary error function (c) Elementary error function (d) Thermal Budget
6	The semiconductor surface contains surface states due to incompletebonds and other effects, which can lead to charges at metal-semiconductor interface. The contact is automatically discontinuity between semiconductor crystal and metal. (a) Covalent, Diminishes (b) Metal, Sharp (c) Metal, Diminishes (d) Covalent, Sharp
7	An important consideration in this process is to isolate the gate from the channel current flow. If the is not isolated from the channel, it draws a lot of current, leading to a device that has a (a) Gate, Poor gain (b) Poor gain, Source (c) High gain, Gate (d) Gate, High gain
8	free carriers are produced in a channel by the process of "inversion" where a gate bias pulls the bandedges to a point lower than the level and thus creates free carriers. (a) FET, Fermi (b) MOFFET, Fermi (c) FET, Conduction (d) MOSFET, Conduction

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