				Seat No. :		
				NH-102		
				November-2013		
				B.Sc. (SemIII)		
				201 : Electronics		
Tim	e: 3	Hou	rs]	[Max. Marks :	: 70	
Inst	ructio	on:	(1)	All questions carry equal marks.		
			(2)	Figures at right hand side denote marks.		
			(3)	Symbols have their usual meaning.		
1.	(a)			transistor amplifier circuit and discuss the effect of emitter bypass on low frequency response.	7	
	(b)	Dra	w CE	hybrid – π model and briefly explain all components.	7	
				OR		
	does	the	lower	you understand by the term 'sag' drawing a diagram if necessary. How 3-dB frequency of an amplifier determine the amount of sag in the ndling a square wave?	14	
2.				ack? Give the general theory of feed back. Why we use negative feed er? Explain in detail.	14	
				OR		
		bac		f negative feed back. Draw neat and clean diagram of current – series explain. Derive the formulas for voltage gain ${\rm AV}_{\rm f}$ and feed back		
3.	char			action of JFET and draw characteristics curve of JFET. Also explain region and pinch off region of JFET. Give the equation of parameters	14	
				OR		

Explain enhancement type and depletion type MOSFET with characteristics and

transfer curve. Also give symbols of both types of MOSFET.

4.	why filter circuit is necessary? Draw neat and clean diagram and wave form for wave rectifier with capacitor filter. Derive the formula for ripple factor.					
		OR				
		w the circuit diagram of Zener diode shunt regulation and explain. Also explain ation in regulation with varying input voltage and load resistance.				
5.	Give	the answer:	14			
	(1)	Draw frequency response curve of an CE amplifier.				
	(2)	What is Sag?				
	(3)	What is base spreading resistance?				
	(4)	Write full form of MOSFET.				
	(5)	Is FET current controlled device or not?				
	(6)	What is I _{GSS} ?				
	(7)	Give the full form of VVR.				
	(8)	Define Voltage Regulation.				
	(9)	What is feed back factor?				
	(10)	Write formula for load resistance.				
	(11)	Why bleeder resistance used in L-C filter?				
	(12)	Give the equation of ripple in inductor filter.				
	(13)	Why voltage shunt feed back not appreciable?				
	(14)	State any two disadvantages of zener diode shunt regulator.				

NH-102 2