SL-134

September-2020

LL.M, Sem.-II

411 E: Industrial Design, Semi-Conductor Layout Design (Intellectual Property Rights)

Time: 2 Hours [Max. Marks: 60 **Instructions:** (1) All questions carry equal marks. (2) Answer any three questions. "Design is the mid-point between Patent on one side and Trademark and Copyright on 1. the other side." Discuss the statement critically. 20 20 2. Discuss briefly: (a) Intellectual Property Rights aspect of Design (b) Functional Design 3. Critically evaluate the Design Act, 2000. 20 4. Write notes on: 20 Infringement of Copyright design (b) License and assignment of design 5. Discuss briefly the international instruments regulating Semi-Conductor Integrated Circuit Layout Design. 20 6. Write short note on: 20 Salient features of Semi-Conductor Integrated Circuit Layout Design Act. Offences and Penalties for infringement of Semi-Conductor Integrated Circuit Layout Design.